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The Xilinx Virtex-5 FXT device provides a tightly coupled integration of processor subsystem, DSP-enabled FPGA fabric, and high-speed communication. Such high levels of integration have allowed both the hardware and software elements of the LTE baseband reference system to be integrated on a single Xilinx FX70T part using standard hardware boards.

Implementing LTE on FPGAs | EE Times

Here's a review of the LTE algorithms and a practical implementation on a Xilinx FPGA. The reference design is tested using multiple video stream with varying encoding rates. By Rob Payne, Xilinx

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dspdesignline.com (February 06, 2009)

The next generation of the 3GPP wireless standard is called long-term evolution (LTE). It provides a leap in performance and a complete move to packet-based processing.

Implementing LTE on FPGAs - Design And Reuse

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FPGA Implementation of LTE Downlink
Transceiver with Synchronization and
Equalization Sara M. Hassan Abdelhalim
Zekry Modern Academy, Cairo, Egypt

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Ain Shams University, Cairo, Egypt

ABSTRACT Long Term Evolution (LTE) is an advanced standard of the mobile communication systems. LTE has been developed by the 3rd Generation Partnership Project (3GPP).

FPGA Implementation of LTE Downlink Transceiver with ...

In this paper, we have filled this gap of unavailability of actual hardware implementation of a UFMC transmitter. Hence, first real time FPGA implementation of UFMC transmitter complying with the timing requirements of 10MHz channelization of LTE is presented here.

FPGA Implementation of UFMC Based Baseband Transmitter ...

This paper presents the FPGA (Field Programmable Gate Array)

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implementation simulation results for Turbo encoder and decoder structure for 3GPP-LTE standard. The proposed architecture of this paper analysis the logic size, area and power consumption using Xilinx 14.2. List of the following materials will be included with the Downloaded Backup: 1.

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The Turbo Decoder in Wireless HDL

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Toolbox™ is a Simulink® building block for use in FPGA or ASIC designs that need to deliver LTE signal information to your application. Typically, these designs start as algorithms in MATLAB® and LTE Toolbox™. Learn how to use your MATLAB based test environment to drive your Simulink based hardware implementation model and compare the results against your algorithmic golden reference model.

Verifying an FPGA Implementation of an LTE Turbo Decoder ...

Read Free Fpga Implementation Of An Lte Based Ofdm Transceiver For implementation of Turbo Decoder is done on the Field Programmable Gate Array (FPGA), due to its low cost and very short development cycle. The design is coded in the verilog hardware programming language and simulated using Xilinx®

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Fpga Implementation Of An Lte Based Ofdm Transceiver For developed to design SoC on a heterogeneous FPGA CPU platform on the basis of performance metrics such as area, power, and latency. Design of physical downlink shared channel (PDSCH) in long term evolution (LTE) is presented as a case study. This paper provides the implementation of the transmitter

Automated performance based design technique for an ...

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FPGA Implementation of Video

Transmission System Based on LTE

Implementation of an efficient turbo decoder with low complexity, short delay and insignificant performance degradation is currently a quite challenging task. The paper presents an implementation of a 3GPP TS 36.212 LTE turbo decoder. The design of the turbo decoder has been optimized to achieve efficient FPGA resource utilization.

FPGA implementation of LTE turbo decoder using MAX-log MAP ...

FPGA Implementation of Turbo Decoder for LTE Standard . By S. Rajaram, A.

Sakthi Amutha Vardhini and K. Kalyani.

Abstract. The data rate of 100 Mbps will be supported by upcoming 3G Long Term Evolution (LTE) standard. In 20 MHz of bandwidth, this data rate will be attained. For the arrival of high data rate of the 3G

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For

FPGA Implementation of Turbo Decoder
for LTE Standard - CORE

Abstract This paper describes the implementation on field programmable gate array (FPGA) of a turbo decoder for 3GPP Long-Term Evolution standard. Considering the high data rates required by this standard, parallel decoding architecture is used.

This chapter describes the implementation on field programmable gate array (FPGA) of a turbo decoder for 3GPP long-term evolution (LTE) standard, respectively, for IEEE 802.16-based WiMAX systems. We initially present the serial decoding architectures for the two systems. The same approach is used; although for

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WiMAX the scheme implements a duobinary code, while for LTE a binary code is included. The proposed LTE serial decoding scheme is adapted for parallel transformation. Then, considering the LTE high throughput requirements, a parallel decoding solution is proposed. Considering a parallelization with $N = 2^p$ levels, the parallel approach reduces the decoding latency N times versus the serial decoding one. For parallel approach the decoding performance suffers a small degradation, but we propose a solution that almost eliminates this degradation, by performing an overlapped data block split. Moreover, considering the native properties of the LTE quadratic permutation polynomial (QPP) interleaver, we propose a simplified parallel decoder architecture. The novelty of this scheme is that only one interleaver module is used, no matter the value of N , by introducing

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an even-odd merge sorting network. We propose for it a recursive approach that uses only comparators and subtractors.

This book constitutes the refereed proceedings of the 13th EAI International Conference on Cognitive Radio Oriented Wireless Networks, CROWNCOM 2018, held in Ghent, Belgium, in September 2018. The 20 revised full papers were selected from 26 submissions. The papers are organized thematically in tracks: Experimental, Licensed Shared Access and Dynamic Spectrum Access, and PHX and Sensing.

A practical guide to LTE design, test and measurement, this new edition has been updated to include the latest developments
This book presents the latest details on

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LTE from a practical and technical perspective. Written by Agilent's measurement experts, it offers a valuable insight into LTE technology and its design and test challenges. Chapters cover the upper layer signaling and system architecture evolution (SAE). Basic concepts such as MIMO and SC-FDMA, the new uplink modulation scheme, are introduced and explained, and the authors look into the challenges of verifying the designs of the receivers, transmitters and protocols of LTE systems. The latest information on RF and signaling conformance testing is delivered by authors participating in the LTE 3GPP standards committees. This second edition has been considerably revised to reflect the most recent developments of the technologies and standards. Particularly important updates include an increased focus on LTE-Advanced as well as the

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latest testing specifications. Fully updated to include the latest information on LTE 3GPP standards Chapters on conformance testing have been majorly revised and there is an increased focus on LTE-Advanced Includes new sections on testing challenges as well as over the air MIMO testing, protocol testing and the most up-to-date test capabilities of instruments Written from both a technical and practical point of view by leading experts in the field

This edited volume "Field-Programmable Gate Array" is a collection of reviewed and relevant research chapters, offering a comprehensive overview of recent developments in the field of semiconductors. The book comprises single chapters authored by various researchers and edited by an expert active in the aerospace engineering systems

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research area. All chapters are complete within themselves but united under a common research study topic. This publication aims at providing a thorough overview of the latest research efforts by international authors and open new possible research paths for further novel developments.

This book constitutes the proceedings of three International Conferences, NeCoM 2011, on Networks & Communications, WeST 2011, on Web and Semantic Technology, and WiMoN 2011, on Wireless and Mobile Networks, jointly held in Chennai, India, in July 2011. The 74 revised full papers presented were carefully reviewed and selected from numerous submissions. The papers address all technical and practical aspects of networks and communications in wireless and mobile networks dealing with

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issues such as network protocols and wireless networks, data communication technologies, and network security; they present knowledge and results in theory, methodology and applications of the Web and semantic technologies; as well as current research on wireless and mobile communications, networks, protocols and on wireless and mobile security.

This book gathers the proceedings of the Third International Conference on Computational Advancement in Communication Circuits and Systems (ICCACCS 2020), organized virtually by Narula Institute of Technology, Kolkata, India. The book presents peer-reviewed papers that highlight new theoretical and experimental findings in the fields of electronics and communication engineering, including interdisciplinary areas like advanced computing, pattern

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recognition and analysis, and signal and image processing. The respective papers cover a broad range of principles, techniques, and applications in microwave devices, communication and networking, signal and image processing, computations and mathematics, and control.

An introduction to technical details related to the Physical Layer of the LTE standard with MATLAB® The LTE (Long Term Evolution) and LTE-Advanced are among the latest mobile communications standards, designed to realize the dream of a truly global, fast, all-IP-based, secure broadband mobile access technology. This book examines the Physical Layer (PHY) of the LTE standards by incorporating three conceptual elements: an overview of the theory behind key enabling technologies; a concise discussion regarding standard specifications; and the

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MATLAB® algorithms needed to simulate the standard. The use of MATLAB®, a widely used technical computing language, is one of the distinguishing features of this book. Through a series of MATLAB® programs, the author explores each of the enabling technologies, pedagogically synthesizes an LTE PHY system model, and evaluates system performance at each stage. Following this step-by-step process, readers will achieve deeper understanding of LTE concepts and specifications through simulations. Key Features:

- Accessible, intuitive, and progressive; one of the few books to focus primarily on the modeling, simulation, and implementation of the LTE PHY standard
- Includes case studies and testbenches in MATLAB®, which build knowledge gradually and incrementally until a functional specification for the LTE PHY is attained
- Accompanying Web site

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includes all MATLAB®

programs, together with PowerPoint slides and other illustrative examples Dr

Houman Zarrinkoub has served as a development manager and now as a senior product manager with MathWorks, based in Massachusetts, USA. Within his 12 years at MathWorks, he has been responsible for multiple signal processing and communications software tools. Prior to MathWorks, he was a research scientist in the Wireless Group at Nortel Networks, where he contributed to multiple standardization projects for 3G mobile technologies. He has been awarded multiple patents on topics related to computer simulations. He holds a BSc degree in Electrical Engineering from McGill University and MSc and PhD degrees in Telecommunications from the Institut Nationale de la Recherche Scientifique, in Canada. <http://www>

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Wireless communication is continuously evolving to improve and be a part of our daily communication. This leads to improved quality of services and applications supported by networking technologies. We are now able to use LTE, LTE-Advanced, and other emerging technologies due to the enormous efforts that are made to improve the quality of service in cellular networks. As the future of networking is uncertain, the use of deep learning and big data analytics is a point of focus as it can work in many capacities at a variety of levels for wireless communications. Implementing Data Analytics and Architectures for Next Generation Wireless Communications addresses the existing and emerging theoretical and practical challenges in the

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design, development, and implementation of big data algorithms, protocols, architectures, and applications for next generation wireless communications and their applications in smart cities. The chapters of this book bring together academics and industrial practitioners to exchange, discuss, and implement the latest innovations and applications of data analytics in advanced networks. Specific topics covered include key encryption techniques, smart home appliances, fog communication networks, and security in the internet of things. This book is valuable for technologists, data analysts, networking experts, practitioners, researchers, academicians, and students.

Conference on Telecommunications

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